

ABSTRACT

A semiconductor apparatus is provided in which input/output of an electric signal having a particularly high frequency is less disturbed by parasitic capacitance generated in a wiring in the semiconductor apparatus. A first through-hole wiring penetrating a first dielectric board, a second through-hole wiring penetrating a second dielectric board, and an internal wiring inserted between the first dielectric board and the second dielectric board are provided. The first through-hole wiring and the second through-hole wiring are arranged on the internal wiring while being away from each other.